IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

h re the Application of: Hiroshi YOSHIDA

Group Art Unit: Not yet assigned

Serial No.: 10/518,391

Examiner: Not yet assigned

Filed: December 17, 2004

Confirmation No.: Not yet assigned

For: MAGNETORESISTIVE RANDOM-ACCESS MEMORY DEVICE

Attorney Docket Number: 043025

Customer Number: 38834

SUBMISSION OF ENGLISH TRANSLATION OF IPER

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

July 15, 2005

Sir:

Submitted herewith is an English translation of the International Preliminary Examination Report for the above-identified U.S. patent application. Please note that the cited references of the International Preliminary Examination Report were previously filed with the Information Disclosure Statement on December 17, 2004.

If any additional fees are due in connection with this submission, please charge our Deposit Account No. 50-2866.

Respectfully submitted,

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Enclosure: Form PCT/IB/338



From the INTERNATION JUREAU

PCT

NOTIFICATION OF TRANSMITTAL
OF COPIES OF TRANSLATION
OF THE INTERNATIONAL PRELIMINARY
EXAMINATION REPORT

(PCT Rule 72.2)

To:

NISHI, Yoshiyuki Nishi Patent Office Suite 211, 26-32, Nakahara 4-chome Isogo-ku Yokohama-shi, Kanagawa 235-0036 JAPON

Date of mailing (day/month/year) 09 December 2004 (09.12.2004)		
Applicant's or agent's file reference YG2003-15PCT	IMPORTANT NOTIFICATION	
International application No. PCT/JP2003/007447	International filing date (day/month/year) 11 June 2003 (11.06.2003)	
Applicant JAPAN SCIENC	CE AND TECHNOLOGY AGENCY et al	

1. Transmittal of the translation to the applicant.

The International Bureau transmits herewith a copy of the English translation made by the International Bureau of the international preliminary examination report established by the International Preliminary Examining Authority.

2. Transmittal of the copy of the translation to the elected Offices.

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3. Reminder regarding translation into (one of) the official language(s) of the elected Office(s).

The applicant is reminded that, where a translation of the international application must be furnished to an elected Office, that translation must contain a translation of any annexes to the international preliminary examination report.

It is the applicant's responsibility to prepare and furnish such translation directly to each elected Office concerned (Rule 74.1). See Volume II of the PCT Applicant's Guide for further details.

The International Bureau of WIPO 34, chemin des Colombettes 1211 Geneva 20, Switzerland

Authorized officer

Yoshiko Kuwahara

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INTERNATIONAL PRELIMINARY EXAMINATION REPORT

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ans.	NTERNATIONAL PRELIMI	NARY EXAMINA	ATION REPORT
	(PCT Article	36 and Rule 70)	
Applicant's or agent's file re YG2003-15PC	I KOK BUKITEKA	CTION See Notific	cation of Transmittal of Internation Examination Report (Form PCT/IPEA/41
International application No. PCT/JP2003/00	International filing da	ate (day/month/year) (11.06.2003)	Priority date (day/month/year) 18 June 2002 (18.06.2002)
International Patent Classific H01L 27/105, 43/	eation (IPC) or national classification at 08, 43/12, G11C 11/15	nd IPC	
Applicant	JAPAN SCIENCE AND T	ECHNOLOGY A	GENCY
This international pand is transmitted to	reliminary examination report has been to the applicant according to Article 36.	prepared by this Intern	national Preliminary Examining Authority
2. This REPORT cons	ists of a total of 3 sheet:	s, including this cover s	sheet.
amended and	s also accompanied by ANNEXES, i.e. I are the basis for this report and/or she action 607 of the Administrative Instruc	ets containing rectification	on, claims and/or drawings which have be ations made before this Authority (see R
These annex	es consist of a total of2	sheets.	**
3. This report contains	s indications relating to the following it	ems:	
I 🔀 Bas	sis of the report		
II Prio	ority		
III No:	n-establishment of opinion with regard	to novelty, inventive s	tep and industrial applicability
IV Lac	ck of unity of invention		
v 🛭 Rea	asoned statement under Article 35(2) wations and explanations supporting such	ith regard to novelty, in statement	nventive step or industrial applicability;
VI Cer	tain documents cited		,
المبسيا الله	tain defects in the international applica	ution	
	rtain observations on the international a	pplication	
Date of submission of the o	lemand	Date of completion	of this report
26 Decemb	per 2003 (26.12.2003)	16	5 June 2004 (16.06.2004)
Name and mailing address	of the IPEA/JP	Authorized officer	
		Telephone No.	



Internation application No.

PCT/JP2003/007447

. Basis	. Basis of the report						
1. With regard to the elements of the international application:*							
	the international application as originally filed						
$\overline{\boxtimes}$	the description:	•					
لاست	pages l-22	, as originally filed					
	pages	, filed with the demand					
	pages , filed with the lette	er of					
$ \nabla $	the claims:	l e e e e e e e e e e e e e e e e e e e					
لاسكا	pages 2-11	, as originally filed					
	pages , as amended (t	together with any statement under Article 19					
	pages 12,13	, filed with the demand					
	pages, filed with the letter	er of					
$ \nabla $	the drawings:						
لاعا	pages 1/4-4/4	, as originally filed					
	pages	, filed with the demand					
	pages, filed with the letter	er of					
	the sequence listing part of the description:						
لــا		as originally filed					
	pages	, filed with the demand					
	pages, filed with the letter	er of					
 With regard to the language, all the elements marked above were available or furnished to this Authority in the language in which the international application was filed, unless otherwise indicated under this item. These elements were available or furnished to this Authority in the following language							
	The statement that the subsequently furnished written sequence listing de international application as filed has been furnished. The statement that the information recorded in computer readable form is in been furnished.						
4. 🔀	the description, pages the claims, Nos1 the drawings, sheets/fig						
5.	This report has been established as if (some of) the amendments had not been rebeyond the disclosure as filed, as indicated in the Supplemental Box (Rule 70.20)	c)).**					
in i	* Replacement sheets which have been furnished to the receiving Office in response to an invitation under Article 14 are referred to in this report as "originally filed" and are not annexed to this report since they do not contain amendments (Rule 70.16 and 70.17).						
** Any	** Any replacement sheet containing such amendments must be referred to under item 1 and annexed to this report.						



International application No.

PCT/JP03/07447

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

V. Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement 1. Statement						
Claims		NO NO				
Claims	2-13	YES				
Claims		NO NO				
Claims	· 2-13	YES				
Claims		NO				
	Claims Claims Claims Claims Claims Claims Claims	Claims Claims Claims Claims Claims Claims 2-13 Claims 2-13 Claims 2-13				

2. Citations and explanations

List of Documents Cited

- 1. JP, 2000-196030, A (YAMAHA CORPORATION), 14 July 2000 (14.07.00), full text (Family: none)
- 2. US, 2002/0057594, À1 (TADAHIKO HIRAI), 16 May 2002 (16.05.02), full text & JP, 2002-141481, A & JP, 2002-140889, A & JP, 2002-170375, A
- 3. Magnetotransport properties of a room temperature rectifying tunnel junction made of electron and hole doped manganites (C. MITRA, ET AL.), Journal of Applied Physics, 15 May 2002 (15.05.02), Vol. 91, No. 10, pages 7715-7717

Claims 2-13

None of the documents cited in the ISR including documents 1-3 listed above describes a magnetoresistive random-access memory device that provides a TMR element with a switch effect utilizing the rectification effect due to the p-n low-resistance tunneling magnetoresistance effect due to the junction between a p-type halfmetallic ferromagnetic semiconductor and an n-type halfmetallic ferromagnetic semiconductor; a magnetoresistive random-access memory device that provides a TMR element with a switch effect utilizing the rectification effect through a p-i-n low-resistance tunneling magnetoresistive (low-resistance TMR) diode in which the p-type halfmetallic ferromagnetic semiconductor consists of a group II-VI compound semiconductor doped with Cr and holes, the n-type halfmetallic ferromagnetic semiconductor consists of the aforesaid group II-VI compound semiconductor doped with V and electrons, and at least one layer of a nonmagnetic insulator atom layer (i layer) is sandwiched therebetween; a magnetoresistive random-access memory device that provides a TMR element with a switch effect utilizing the rectification effect through a p-i-n low-resistance tunneling magnetoresistive (low-resistance TMR) diode in which the p-type halfmetallic ferromagnetic semiconductor consists of a group III-V compound semiconductor doped with Mn and holes, the n-type halfmetallic ferromagnetic semiconductor consists of the aforesaid group III-V compound semiconductor doped with Cr and electrons, and at least one layer of a nonmagnetic insulator atom layer (i layer) is sandwiched therebetween; a magnetoresistive random-access memory device that provides a TMR element with a switch effect utilizing the rectification effect through a p-i-n low-resistance tunneling magnetoresistive (low-resistance TMR) diode in which the p-type halfmetallic ferromagnetic semiconductor consists of ZnO doped with Cr and holes, the n-type halfmetallic ferromagnetic semiconductor consists of ZnO doped with V, Fe, Co or Ni and electrons, and at least one layer of a nonmagnetic insulator atom layer (i layer) is sandwiched therebetween; or a magnetoresistive random-access memory device that provides a TMR element with a switch effect utilizing the rectification effect through a p-i-n low-resistance tunneling magnetoresistive (low-resistance TMR) diode in which the p-type halfmetallic ferromagnetic semiconductor consists of a group IV semiconductor doped with Fe and holes, the n-type halfmetallic ferromagnetic semiconductor consists of the aforesaid group IV semiconductor doped with Mn and electrons, and at least one layer of a nonmagnetic insulator atom layer (i layer) is sandwiched therebetween, and it is not obvious to a person skilled in the art.